Attorney Docket No.: P16909

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: HE et al.	Examiner: Not yet assigned		
Serial No.: Not yet assigned) Group Art Unit: Not yet assigned		
Filing Date: Herewith	Attorney Docket No.: P16909		
For: I/O ARCHITECTURE FOR INTEGRATED CIRCUIT PACKAGE))))		

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants submit herewith patents, publications or other information of which they are aware that they believe may be material to the examination of this application, and in respect of which, there may be a duty to disclose.

The filing of this information disclosure statement shall not be construed as a representation that a thorough search has been made, an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists. Nor shall the filing of this information disclosure statement be construed as an admission against interest in any manner.

This Information Disclosure Statement is filed in accordance with 37 CFR §§1.56, 1.97 and 1.98. The items listed on the accompanying Form PTO-1449 may be deemed to be pertinent to the above-identified application and are made of record to assist the Patent and Trademark Office in its examination of this application. The Examiner is respectfully requested to fully consider the items listed on the enclosed copy of Form PTO-1449 and to independently ascertain their teaching.

No fee is believed to be due under 37 CFR §1.17(p) for this Information Disclosure Statement since it is being filed concurrently with the above-identified application.

Respectfully submitted,

September 8, 2003

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Substitute for form 1449A/PTO		Complete if Known			
				Application Numb r	Not yet assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Dat	Herewith		
		First Named Inventor	HE, Jianqi		
		Group Art Unit	Not yet assigned		
		Examiner Name	Not yet assigned		
Sheet	1	of	1	Attorney Docket Number	P16909

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
	A	Mahajan, Ravi et al., "The Evolution of Microprocessor Packaging", Intel Technology Journal Q3, 2000. 10pgs.			
	В	Lii, Mirng-Ji et al., "Flip-Chip Technology on Organic Pin Grid Array Packages", Intel Technology Journal Q3, 2000. 9pgs.			
	С	Polka, Lesley et al., "Package-Level Interconnect Design for Optimum Electrical Performance", Intel Technology Journal Q3, 2000. 17pgs.			

Examiner	Date	
Signature	Considered	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.